

FORWARD ERROR CORRECTION SCHEME FOR
HIGH RATE DATA EXCHANGE IN A WIRELESS SYSTEM

ABSTRACT OF THE DISCLOSURE

A transmitter/receiver system for high data transfer in a wireless communication
5 system includes a physical layer processor that comprises an FEC coder, a demultiplexer
and a plurality of modem processors. The FEC coder applies error correction codes to
the high data rate signal. Thereafter, the demultiplexer distributes portions of the coded
high data rate signal to the modem processors. Each modem processor processes its
respective portion of the coded signal for transmission in an independent channel.